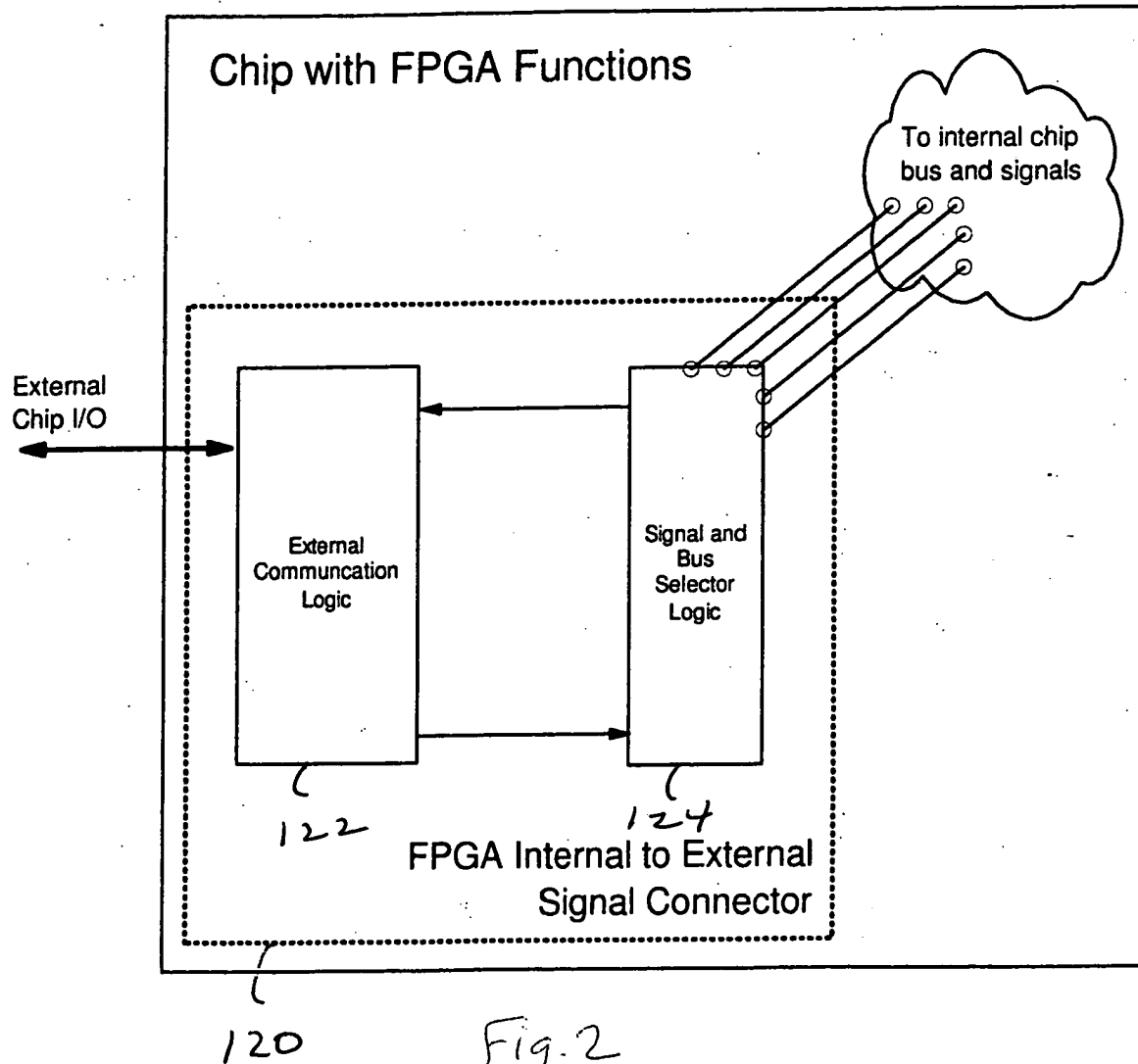


Fig. 1



FOOTPRINT "B4T3T00T"

Monitoring internal bus
reveals Function A is not
sending correct values to
Function B.

Drive correct values into
Function B from FPGA
and monitor output of
Function B to determine
if it is operating correctly

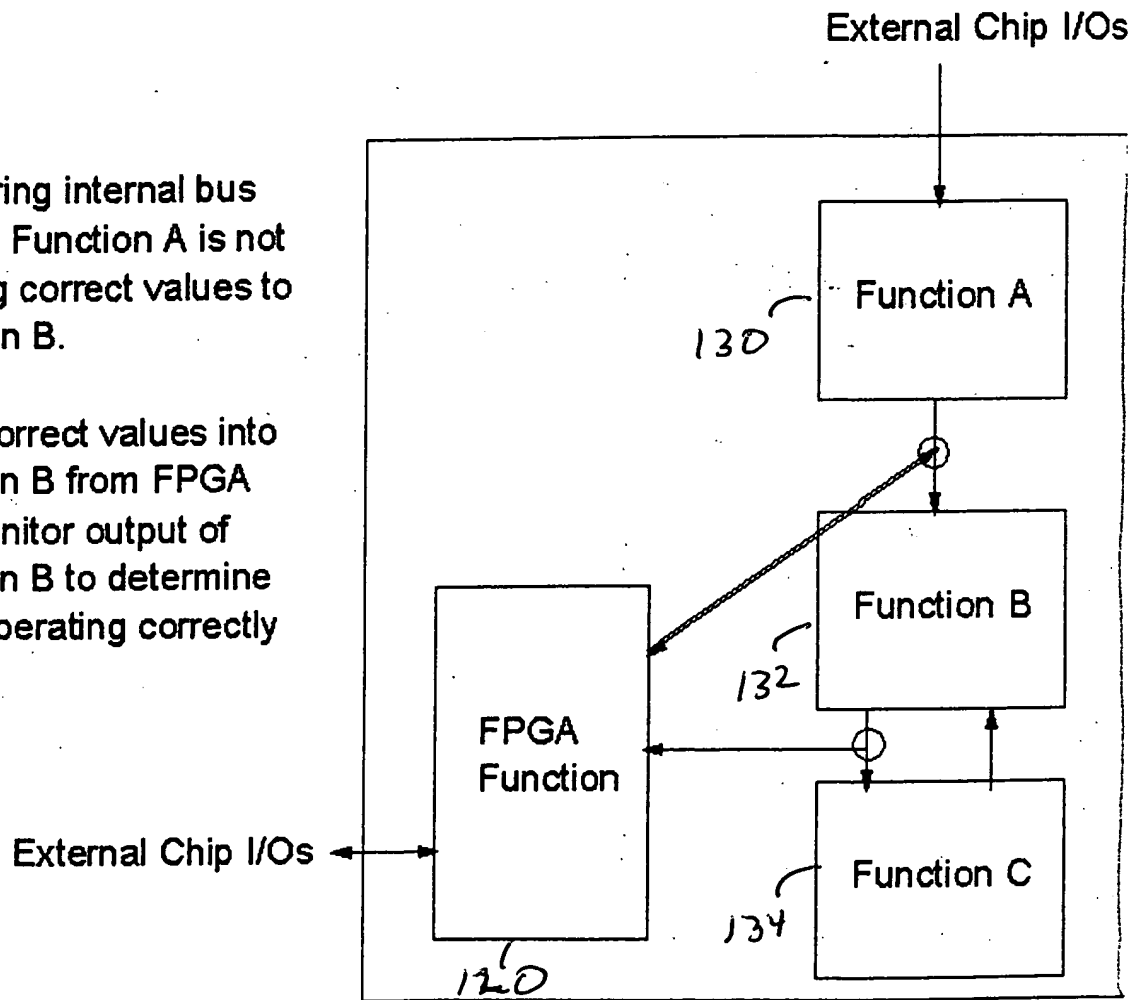


Fig. 3

- Memory and Memory Bus Debug
- MAC Bus Debug
- PCI, Internal Bus and Bus Arbiter Debug
- Processor Debug
- Clock and Clock Distribution Debug

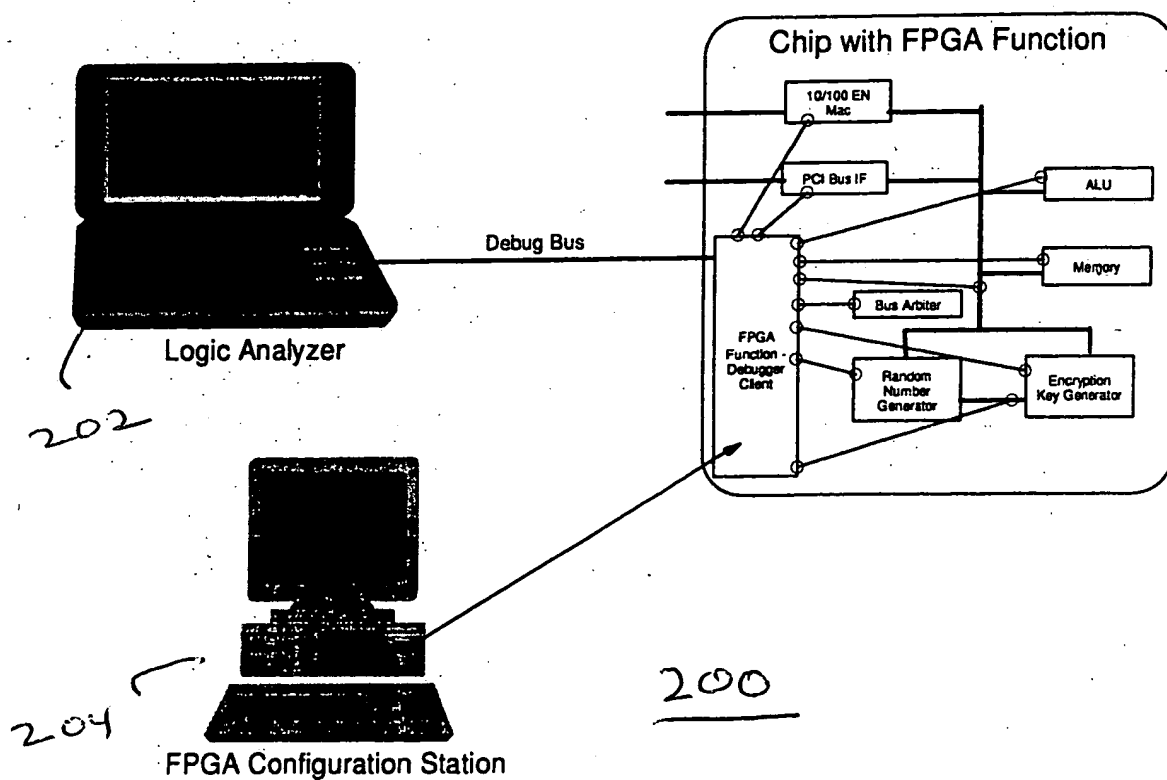
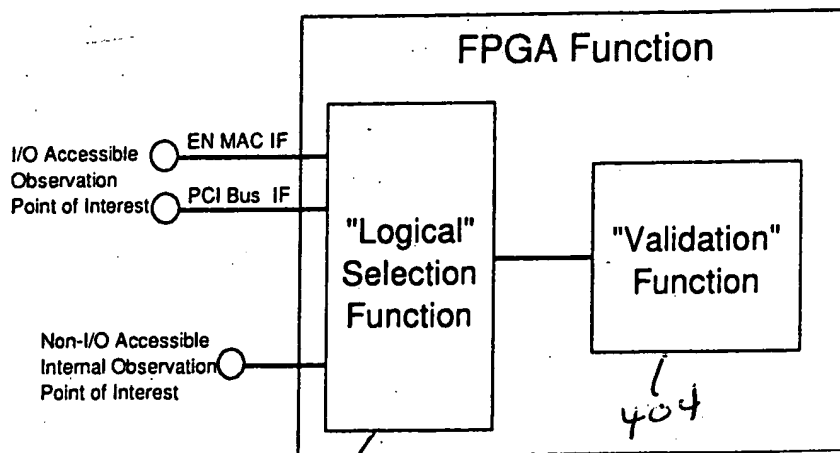


Fig. 4



402 Fig. 5

10016448.121001

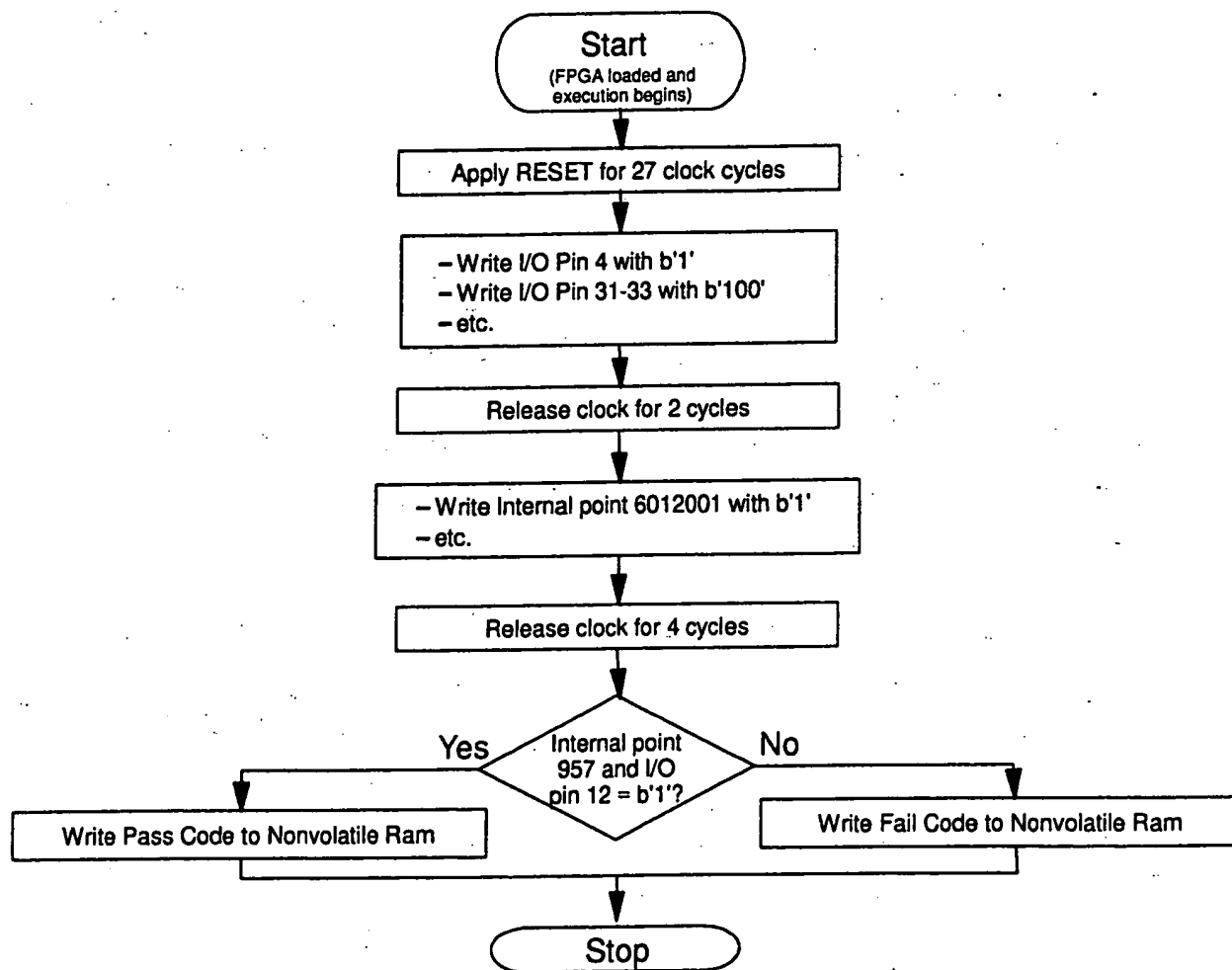


Fig. 6

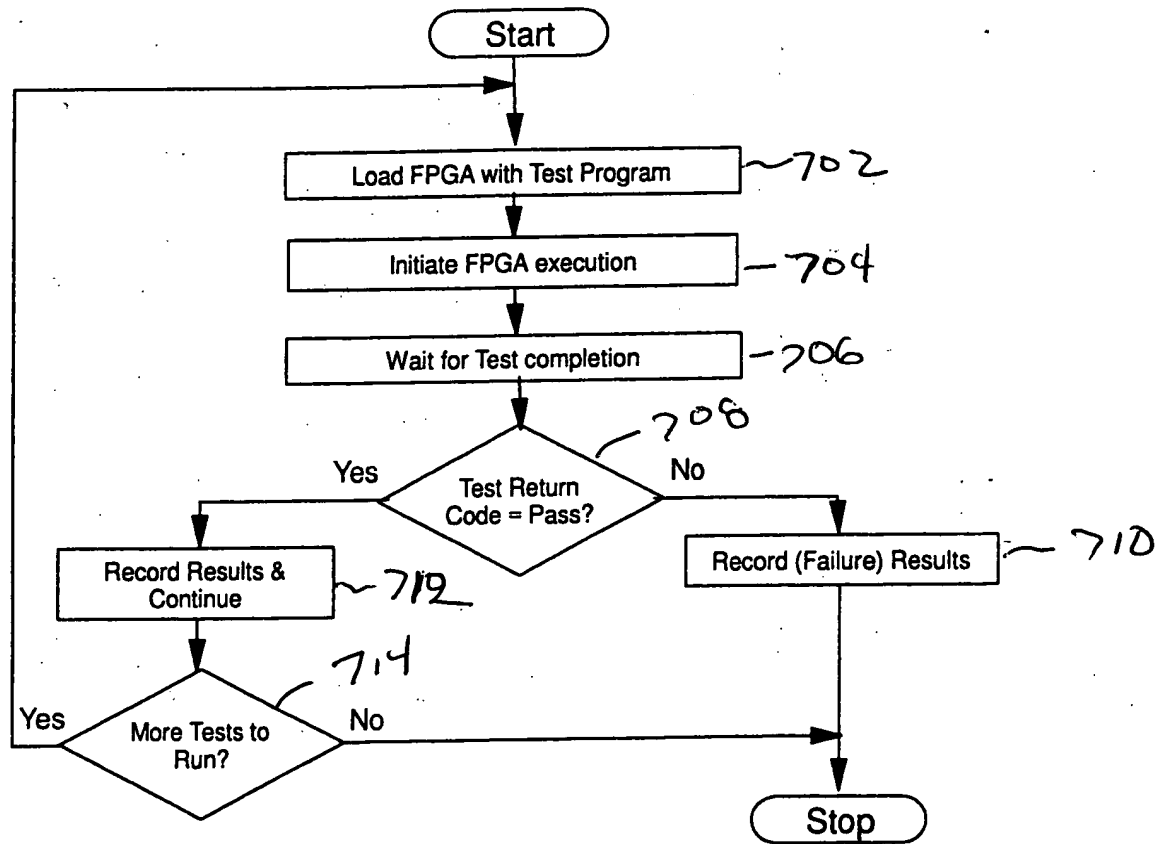


Fig. 7

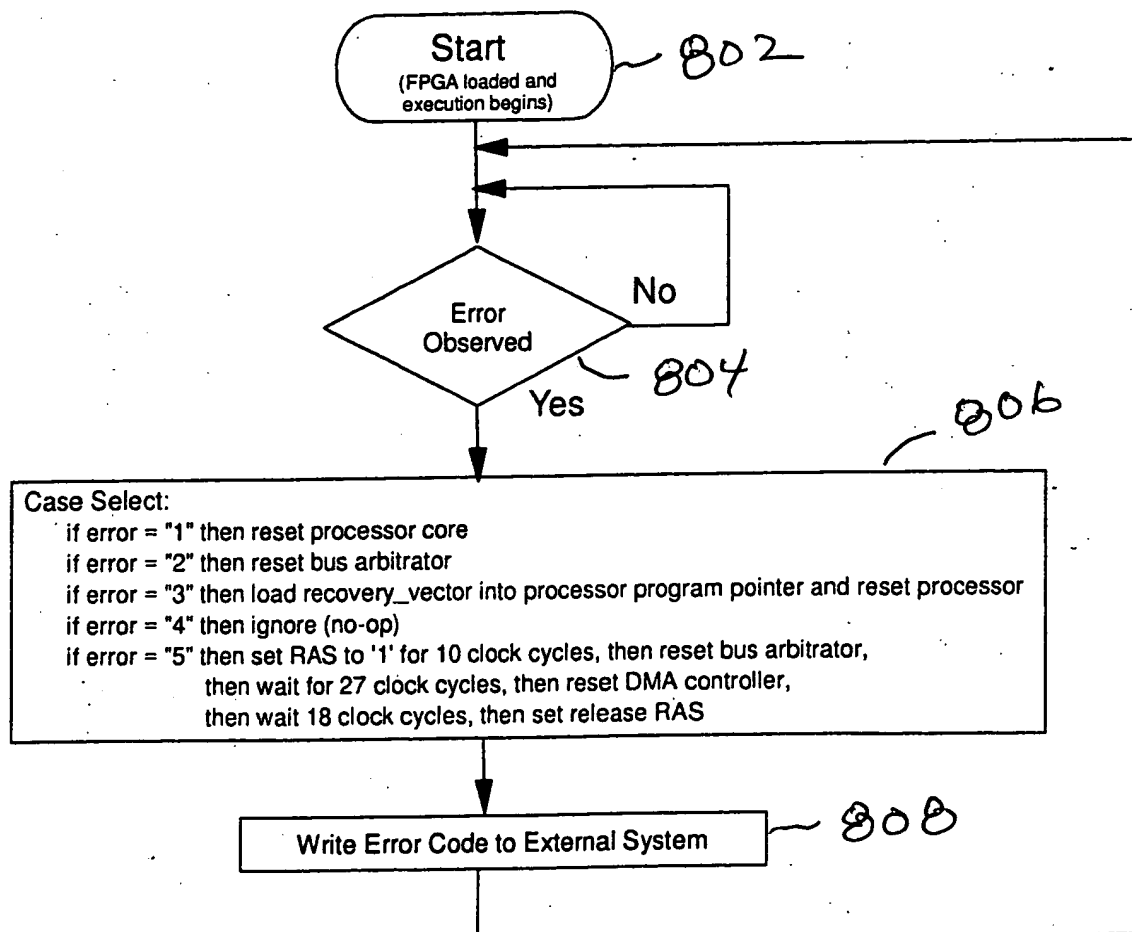


Fig. 8

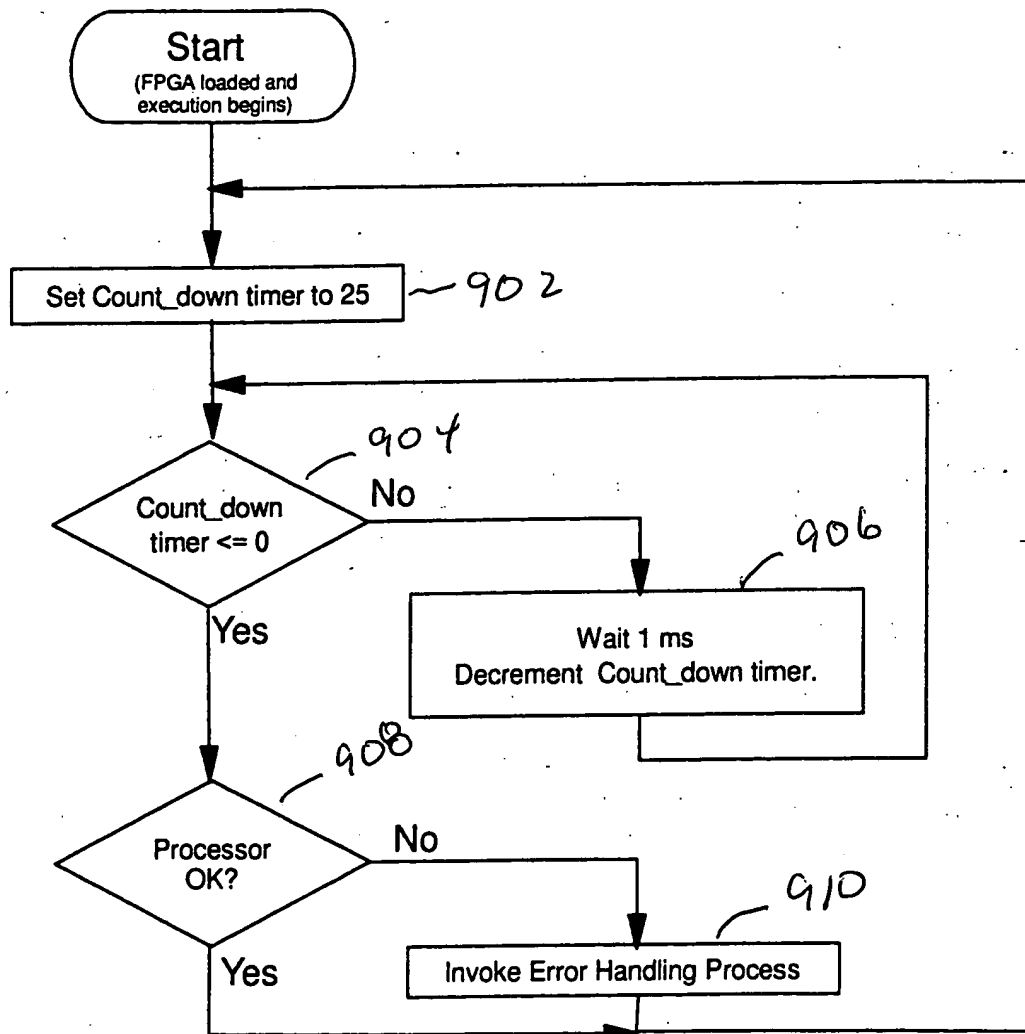


Fig. 9